

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Steven L. Scott et al.

Title: MULTISTREAM PROCESSING MEMORY-AND BARRIER-SYNCHRONIZATION  
METHOD AND APPARATUS

Docket No.: 1376.733US1  
Filed: August 18, 2003  
Examiner: Unknown

Serial No.: 10/643,741  
Due Date: N/A  
Group Art Unit: 2153

**MS Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 4 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer Number 21186

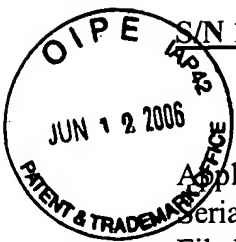
By: Thomas F. Brennan  
Atty: Thomas F. Brennan  
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 8th day of June, 2006.

JAMES KANYUSIK  
Name

Thomas F. Brennan  
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
(GENERAL)



S/N 10/643,741



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Steven L. Scott et al.	Examiner:	Unknown
Serial No.:	10/643,741	Group Art Unit:	2153
Filed:	August 18, 2003	Docket:	1376.733US1
Title:	MULTISTREAM PROCESSING MEMORY-AND BARRIER- SYNCHRONIZATION METHOD AND APPARATUS		

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :10/643,741

Filing Date: August 18, 2003

Title: MULTISTREAM PROCESSING MEMORY-AND BARRIER-SYNCHRONIZATION METHOD AND APPARATUS

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Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any U.S. Patents cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

STEVEN L. SCOTT ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date June 8, 2006

By Thomas F. Brennan  
Thomas F. Brennan  
Reg. No. 35,075

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 8th day of June, 2006.

JAMES KANYSIK  
Name

James Kluth  
Signature

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary.)



Complete if Known

Application Number	10/643,741
Filing Date	August 18, 2003
First Named Inventor	Scott, Steven
Group Art Unit	2153
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 1376.733US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-5,765,009	06/09/1998	Ishizaka, K.	01/08/1997
	US-6,308,316	10/23/2001	Hashimoto, S. , et al.	01/08/1998

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"Msync - Synchronise Memory with Physical Storage", <u>The Single UNIX Å?Å® Specification, Version 2: Msync</u> , The Open Group, <a href="http://www.opengroup.org/nlinepubs/007908799/xsh/msync.html">http://www.opengroup.org/nlinepubs/007908799/xsh/msync.html</a> , (1997), 3 pgs.	
		COHOON, J., et al., <u>C++ Program Design</u> , 2nd Edition, McGraw-Hill Companies, Inc., (1999), pg. 493	
		PATTERSON, D. A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA, (1996), 699-708	
		PATTERSON, D. A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA, (1996), 194-197	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1448)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached